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	Filing Date		2006-07-26	
	First Named Inventor	Etsuro MORITA		
	Art Unit	2813		
	Examiner Name	Candice CHAN		
Attorney Docket Number		SHX 373		

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	3	9320584	WO	A1	1993-10-14	Gassel et al.		<input checked="" type="checkbox"/>

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4	07226433	JP		1995-08-22	Hashimoto		<input checked="" type="checkbox"/>
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	2	TONG et al., "VLSI SOI Fabrication by SIMOX Wafer Bonding (SWB)." Institute of Electrical and Electronics Engineers (1992), pp. 72-73.	<input type="checkbox"/>
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